



Reply under 37 CFR 1.116  
– Expedited Procedure –  
Technology Center 2193

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant: JANG, Young-Beom      Examiner: DO, Chat C.  
Serial No.: 10/075,643      Art Unit: 2193  
Filed: Feb. 13, 2002      Docket: 8045-36 (PX1344-US/SSD)  
On: LOW POWER CSD LINEAR PHASE FIR FILTER  
ARCHITECTURE USING VIRTUAL COMMON SUBEXPRESSION  
AND FILTER DESIGN METHOD THEREFOR

Mail Stop AF  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

*Do not enter*  
*CD 09/27/05*

AMENDMENT AFTER FINAL UNDER 37 CFR § 1.116

This paper is responsive to the Final Office Action mailed on June 16, 2005.

The Examiner is requested to enter and consider the following amendments and

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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on the date indicated below:

Date: 8-16-05

By:   
Susan A. Guarascio